



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT APPLICATION OF

YAMADA et al.

Appln. No.: 09/993,967

Filed: November 27, 2001

Title: SEMICONDUCTOR DEVICE WITH VERTICAL TRANSISTOR FORMED IN A  
SILICON-ON-INSULATOR SUBSTRATE

Confirmation No.: 8085

Group Art Unit: 2822

Examiner: Lewis, Monica

April 6, 2004

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**AMENDMENT**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 10, 2003 and the Advisory Action dated January 5, 2004 and further to the Amendment dated November 10, 2003 and the Notice of Appeal dated January 12, 2004, please enter the following amendments and remarks: